



SHAHAJIRAO PATIL VIKAS PRATISHTHAN
S. B. PATIL COLLEGE OF ENGINEERING, INDAPUR, DIST: PUNE.
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ACA - R - 07	CLASS TIME TABLE	Academic Year: 2026-27
Rev : 01		Semester: I
Date: 12.06.2015		
Ref: SPVP/SBPCOE/E&TC/2026-27/F2/01		Date: 11.07.2026
Class: BE		Wef: 15.07.2026
Name of Class Teacher: Dr. A.S.Shirkande		Class Room Location : 122,119,118

Time/ Day	9:00am-10:00am	10.00 am to 11 am	11 to 12pm	12.00 pm to 12.50pm	12.50 pm to 1.50pm	1.50 PM TO 2.50 PM	2.50pm to 3.00pm	3.00pm to 4.00pm
MON	RMT(122)	VLSI(122)	CC (119)	LONG BREAK	MIOT-B1(119A),RMT-B2(121),CC-B3(119)		SHORT BREAK	TRAINING /PLACEMENT
TUE	CC(122)	RMT(122)	EPD(119)		VLSI-B1(120),CC-B2(119),RMT-B3(121)			TRAINING /PLACEMENT
WED	MIOT(122)	EPD(122)	RMT(119)		CC-B1(119),MIOT-B2(119A),VLSI-B3(120)			TRAINING /PLACEMENT
THU	EPD(122)	MIOT(122)	VLSI(119)		RMT-B1(121),VLSI-B2(120), MIOT-B3(119)			TRAINING /PLACEMENT
FRI	VLSI(122)	PROJECT STAGE-I			CC(122) MIOT(122)			PROJRCT-STAGE-II(123A)
SAT	AUDIT COURSE-5(119)	PROJECT/TRAINING(123)			TUTORMEET NPTEL			PROJRCT-STAGE-II(123A)

ABBR:	Name of Subject	Subject Incharge	ABBR:	Name of Subject	Subject Incharge
VLSI	VLSI (TH+PR)	Dr.V.U.Bansude	EPD(Elective-IV)	Electronic Product Develop (TH)	Dr.A.S.Shirkande
RMT	RMT (TH+PR)	Prof. C.V. Nalawade	Project	Project Stage _I	Dr.A.S.Shirkande
MIOT(EL-V)	Modernaised IOT (TH+PR)	Prof. S.V.Bansude	Audit course V	Respective Tutor Teacher	Batch B1:ASS B2:SRJ B3:VUB
CC	Cloud Computing (TH+PR)	Prof K.R. Dokhe			

(Prof. P. S. Togrikar)
I/C Time Table

(Dr. V. U. Bansude)
Head of the Department

(Dr. V. S. Nalawade)
Dean of Academic

(Dr. S. T. Shirkande)
Principal

HEAD
Department of E & TC Engineering
S.B. Patil College of Engineering, Indapur.

Dean Academic
S.B. Patil College Of Engineering
Vangali, Tal. Indapur, Dist. Pune

Principal
S B Patil College of Engineering
Vangali, Tal. Indapur Dist- pune